Schottky Barrier Diode

Schottky barrier diodes are optimized for very low forward voltage drop and low leakage current and are used in a wide range of dc-dc converter, clamping and protection applications in portable devices. NSR0340H in a SOD-323 miniature package enables designers to meet the challenging task of achieving higher efficiency and meeting reduced space requirements.

Features

- Very Low Forward Voltage Drop -415 mV @ 100 mA
- Low Reverse Current 0.4 μA @ 25 V V_R
- 250 mA of Continuous Forward Current
- Power Dissipation of 160 mW with Minimum Trace
- Very High Switching Speed
- Low Capacitance $C_T = 6 pF$
- This is a Pb-Free Device

Typical Applications

- LCD and Keypad Backlighting
- Camera Photo Flash
- Buck and Boost dc-dc Converters
- Reverse Voltage and Current Protection
- Clamping & Protection

Markets

- Mobile Handsets
- MP3 Players
- Digital Camera and Camcorders
- Notebook PCs and PDAs
- GPS

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Reverse Voltage	V _R	40	Vdc
Forward Continuous Current (DC)	IF	250	mA
Non-Repetitive Peak Forward Surge Current	I _{FSM}	1.0	Α
ESD Rating: Human Body Model Machine Model	ESD	Class 2 Class A	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

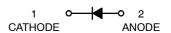
1



ON Semiconductor®

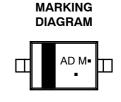
http://onsemi.com

40 VOLT SCHOTTKY BARRIER DIODE





SOD-323 CASE 477 STYLE 1



AD = Specific Device Code

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
NSR0340HT1G	SOD-323* (Pb-Free)	3000/Tape & Reel

^{*}This package is inherently Pb-Free.

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

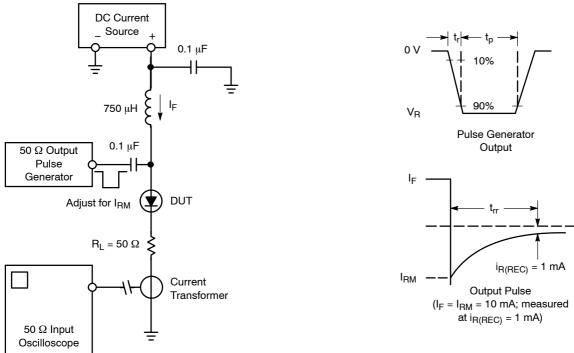
THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance Junction-to-Ambient (Note 1) Total Power Dissipation @ T _A = 25°C	$egin{array}{c} R_{ hetaJA} \ P_D \end{array}$	740 160	°C/W mW
Thermal Resistance Junction-to-Ambient (Note 2) Total Power Dissipation @ T _A = 25°C	R _{θJA} P _D	460 270	°C/W mW
Junction and Storage Temperature Range	T _J , T _{stg}	–55 to +150	°C

- 1. Mounted onto a 4 in square FR-4 board 10 mm sq. 1 oz. Cu 0.06" thick single-sided. Operating to steady state.
- 2. Mounted onto a 4 in square FR-4 board 1 in sq. 1 oz. Cu 0.06" thick single-sided. Operating to steady state.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Leakage (V _R = 10 V) (V _R = 25 V) (V _R = 40 V)	I _R		0.2 0.4 1.3	1.0 3.0 6.0	μА
Forward Voltage (I _F = 10 mA) (I _F = 100 mA) (I _F = 200 mA)	V _F		320 415 470	350 490 590	mV
Total Capacitance (V _R = 10 V, f = 1 MHz)	СТ		6.0		pF
Reverse Recovery Time $(I_F = I_R = 10 \text{ mA}, I_R = 1.0 \text{ mA})$	t _{rr}		5.0		ns



- 1. DC Current Source is adjusted for a Forward Current (I_F) of 10 mA.
- Pulse Generator Output is adjusted for a Peak Reverse Recovery Current I_{RM} of 10 mA.
 Pulse Generator transition time << t_{rr}.
- 4. IR(REC) is measured at 1 mA. Typically 0.1 X I_{RM} or 0.25 X I_{RM}.
- 5. t_p » t_{rr}

Figure 1. Recovery Time Equivalent Test Circuit

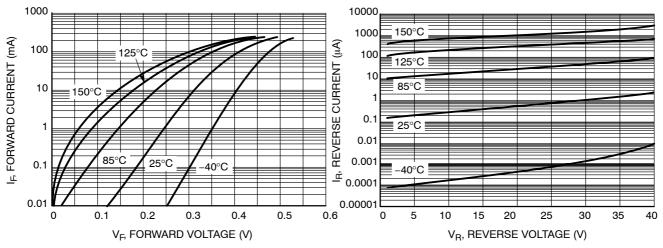


Figure 2. Forward Voltage

Figure 3. Leakage Current

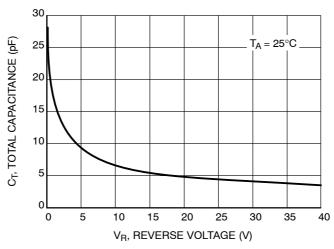
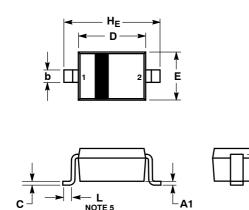


Figure 4. Total Capacitance

PACKAGE DIMENSIONS

SOD-323 CASE 477-02 ISSUF G



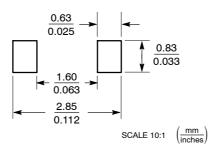
NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: MILLIMETERS.
- 3. LEAD THICKNESS SPECIFIED PER L/F DRAWING WITH SOLDER PLATING.
- DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. DIMENSION L IS MEASURED FROM END OF
- RADIUS.

	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.80	0.90	1.00	0.031	0.035	0.040	
A1	0.00	0.05	0.10	0.000	0.002	0.004	
А3	0.15 REF			0.006 REF			
b	0.25	0.32	0.4	0.010	0.012	0.016	
С	0.089	0.12	0.177	0.003	0.005	0.007	
D	1.60	1.70	1.80	0.062	0.066	0.070	
E	1.15	1.25	1.35	0.045	0.049	0.053	
L	0.08			0.003			
HE	2.30	2.50	2.70	0.090	0.098	0.105	

STYLE 1: PIN 1. CATHODE 2. ANODE

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and 👊 are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice on semiconductor and war engineer trademarks of semiconductor components industries, Ite (SciLLC) solitate services are injective to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

NOTE 3

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA **Phone**: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Japan Customer Focus Center Phone: 81-3-5773-3850

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative